



501.40792VX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Y. YAMAGUCHI, et al.

RECEIVED

Application No.: 09/997,175

FEB 25 2004

Filed: November 30, 2001

For: A MULTI-CHIP MODULE INCLUDING SEMICONDUCTOR
DEVICES AND A WIRING SUBSTRATE FOR MOUNTING
THE SEMICONDUCTOR DEVICES (As Amended)

Art Unit: 2811

Examiner: H. K. Vu

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 20, 2004

Sir:

In response to the Office Action dated November 20, 2003, please amend the
above-identified application as listed below and as set forth on the following pages:

Amendments to the Title;

Amendments to the Claims; and

Remarks are included following the amendments.